

2SK2642-01MR

FUJI POWER MOS-FET

N-CHANNEL SILICON POWER MOS-FET

■ Features

High speed switching

Low on-resistance

No secondary breakdown

Low driving power

High voltage

$V_{GS} = \pm 35V$ Guarantee

Avalanche-proof

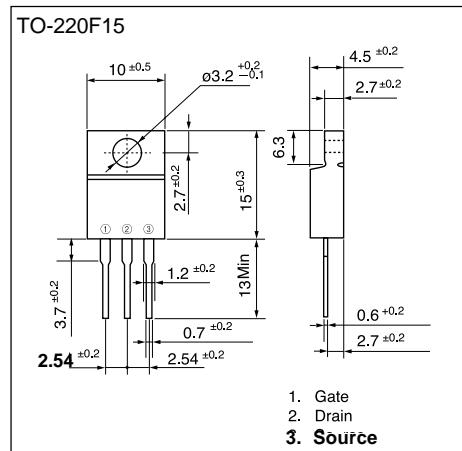
■ Applications

Switching regulators

UPS

DC-DC converters

General purpose power amplifier



■ Maximum ratings and characteristic Absolute maximum ratings

($T_c = 25^\circ\text{C}$ unless otherwise specified)

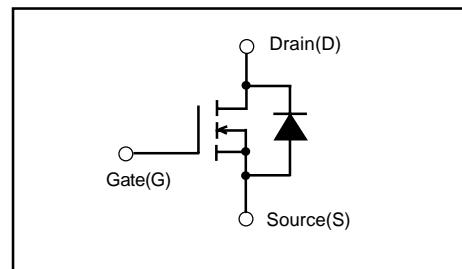
Item	Symbol	Rating	Unit
Drain-source voltage	V_{DS}	500	V
Continuous drain current	I_D	± 15	A
Pulsed drain current	$I_D(\text{puls})$	± 60	A
Gate-source voltage	V_{GS}	± 35	V
Maximum Avalanche Energy	E_{AV}^*	88.7	mJ
Max. power dissipation	P_D	50	W
Operating and storage temperature range	T_{ch}	+150	$^\circ\text{C}$
	T_{stg}	-55 to +150	$^\circ\text{C}$

*1 $L=0.72\text{mH}$, $V_{cc}=50\text{V}$

Electrical characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$ $V_{GS}=0\text{V}$	500			V
Gate threshold voltage	$V_{GS(\text{th})}$	$I_D=1\text{mA}$ $V_{DS}=V_{GS}$		3.5	4.0	4.5
Zero gate voltage drain current	I_{DSS}	$V_{DS}=500\text{V}$ $T_{ch}=25^\circ\text{C}$ $V_{GS}=0\text{V}$ $T_{ch}=125^\circ\text{C}$		10	500	μA
Gate-source leakage current	I_{GSS}	$V_{GS}=\pm 35\text{V}$ $V_{DS}=0\text{V}$		10	100	nA
Drain-source on-state resistance	$R_{DS(on)}$	$I_D=7.5\text{A}$ $V_{GS}=10\text{V}$		0.44	0.55	Ω
Forward transconductance	g_{fs}	$I_D=7.5\text{A}$ $V_{DS}=25\text{V}$	4.5	9.0		S
Input capacitance	C_{iss}	$V_{DS}=25\text{V}$		1400	2100	pF
Output capacitance	C_{oss}	$V_{GS}=0\text{V}$		250	380	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$		110	170	
Turn-on time t_{on}	$t_{d(on)}$	$V_{cc}=300\text{V}$ $I_D=15\text{A}$		30	50	ns
	t_r	$V_{GS}=10\text{V}$		110	170	
Turn-off time t_{off}	$t_{d(off)}$	$R_{GS}=10\Omega$		90	140	
	t_f			55	90	
Avalanche capability	I_{AV}	$L=100\mu\text{H}$ $T_{ch}=25^\circ\text{C}$	15			A
Diode forward on-voltage	V_{SD}	$I_F=2 \times I_D$ $V_{GS}=0\text{V}$ $T_{ch}=25^\circ\text{C}$		1.1	1.65	V
Reverse recovery time	t_{rr}	$I_F=I_D$ $V_{GS}=0\text{V}$		500		ns
Reverse recovery charge	Q_{rr}	$-di/dt=100\text{A}/\mu\text{s}$ $T_{ch}=25^\circ\text{C}$		8.0		μC

■ Equivalent circuit schematic



■ Thermal characteristics

Item	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal resistance	$R_{th(ch-c)}$	channel to case			2.50	$^\circ\text{C}/\text{W}$
	$R_{th(ch-a)}$	channel to ambient			62.5	$^\circ\text{C}/\text{W}$